

SAMXON BRAND ALUMINUM ELECTROLYTIC CAPACITORS

PRODUCT SPECIFICATION

規格書

CUSTOMER: DATE:

(客戶): 志盛翔 (日期): 2019-9-27

CATEGORY (品名) : ALUMINUM ELECTROLYTIC CAPACITORS

DESCRIPTION (型号) : HP 100V2200μF(φ35x30)

VERSION (版本) : 01

Customer P/N :

SUPPLIER :

SUPPLIER						
PREPARED (拟定)	CHECKED (审核)					
赵安平	刘渭清					

CUSTOMER							
APPROVAL	SIGNATURE						
(批准)	(签名)						

ELECTROLYTIC CAPACITOR SPECIFICATION HP SERIES

		SPECIFICAT	ALTERNATION HISTORY RECORDS				
Rev.	Date	Mark	Page	Contents	Purpose	Drafter	Approver
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Name		Specification Sheet – HP				
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Table 1 Product Dimensions and Characteristics

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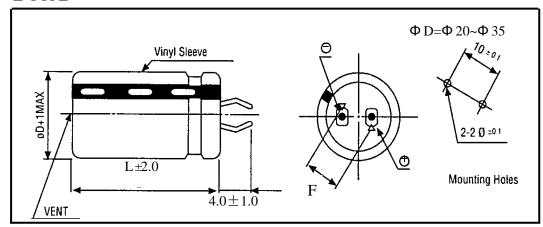


Table 1

No	SAMXON Part No.	WV (Vdc)	Cap (μF)	Cap. tolerance	Temp. range(°C)	tan δ (120Hz, 20°C)	Leakage Current (µA,5min)	Max Ripple Current at 105°C 120Hz (A rms)	Load lifetime (Hrs)	Dimen (m D×L		Sleeve
1	EHP228M2AQ30SZ**P	100	2200	-20%~+20%	-40~105	0.20	1407	2.60	2000	35X30	10±1.0	PET

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Application 1.

This specification applies to polar Aluminum electrolytic capacitor (foil type) used in electronic equipment. Designed capacitor's quality meets IEC60384.

Part Number System

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2. Part Number System										
E G S	S	105	Г	VI	1 H	D 1 1	T C		SA F)
SERIES	CA	PACITAN	CE TOLE	RANCE	VOLTAGE	CASE SIZE	TYPE	SAI	MXON SLE	EVE
- 1		- 1		I	I	I	ı	PROD	UCT LINE MATE	RIAL
Series	Cap (uF)	Code	Tol. (%)	Code	Vol. (W.V.) Code	Case Size	Feature	Code	SAMXON Product	tlino
EKF		i i	±5	J	2 0D	Diameter(⊕) Code		iт	For internal use	—— i I
EKS EGS	0.1	104	l		2.5 OE	3 B 3.5 1	Radial bulk	RR	(The product line	s we
EKM	0.22	224	±10	К	4 0G 6.3 0J	4 C 5 D	Ammo Tap	ing	have H,A,B,C,D,E 0,1,2,3,4,5,9)	
EKG	<u> </u>	\vdash	±15	L	8 0K	6.3 E	3.0mm Pitch			
EOM EGF	0.33	334	±20	м	10 1A	8 F 10 G	2.0mm Pitch	π	Sleeve Material	Code
ESF	0.47	474	±30	N	12.5 1B 16 1C	12.5 I 13 J	2.5mm Pitch	TU	PET	Р
EGT EGK			l	 '` -	20 1D	13.5 V	3.5mm Pitch	τv		=
ESK	1	105	-40 0	w	25 1E	14 4 14.5 A	3.311111 FICCI	''		
ESH	2.2	225	-20	\vdash	30 1I 32 13	16 K	5.0mm Pitch	TC		thesleeve
ERS			-20 0	Α	32 13 35 1V	18 L 18.5 8	Lead Cut &	Form		e m
EGY	3.3	335	-20	С	40 1G	20 M	-	\vdash		eri:
ERR	4.7	475	+10		42 1M 50 1H	22 N 25 O	CB-Type	CB		al is
ERT	4.7	4/3	-20	x	50 IH	30 P	CE-Type	CE		ζ
ERD	10	106	+40		63 1J	35 Q 40 R				material is PVC there will be blank in seventeenth digit.
ERH EBD			-20 +50	s	71 1S	42 4	HE-Type	HE	PVC	r6 ≦.
ERA	22	226	 	$\vdash \vdash \vdash$	75 1T 80 1K	45 6 51 S	KD-Type	KD		■ be
ERB ERC	33	336	-10 0	В	85 1R	63.5 T	ED Trees	FD		ban
EFA		 	-10		90 19 100 2A	80 8	FD-Type	FU		Si
ENP	47	476	+20	V	120 20	90 X 100 Z	EH-Type	EH		seve
ERW	100	107	-10	Q	125 2B	Len. (mm) Code 4.5 45	PCB Termi	inal		2 8
ELP		\vdash	+30		150 2Z 160 2C	5 05	10010111	-		₹ .
EAP	220	227	-10 +50	т	180 2C	7 07		SW		digit
EQP EDP	330	337	l		200 2D	7.7 77 10.2 T2	Snap-in	sx		
ETP		\vdash	+13 +50	E	215 22 220 2N	11 11 11.5 1A				
EHP	470	477	-5	<u> </u>	230 23	12 12 12.5 1B		SZ		
EKP	2200	228	+15	F	250 2E	13 13	Lug	SG		
EPK EEP			-5	G	275 2T 300 2I	13.5 1C 20 20		05		
EFP	22000	229	+20		310 2R	25 25 29.5 2J		03		
ESP	33000	339	0 +20	R	315 2F	30 30 31.5 3A		06		
EGP	<u> </u>			\vdash	330 2U 350 2V	35 35		T5		
EWR EWI	47000	479	0 +30	0	360 2X	35.5 3E 50 50	Screw	\vdash		
EWT	100000	10T	0	\Box	375 2Q	80 80 100 1L		T6		
EWF			+50	_ ' _	385 2Y 400 2G	105 1K		D5		
EWH	150000	15T	+5	z	420 2M	120 1N	i	DC.		
EWL	220000	22T	+15		450 2W	130 1P 140 1Q	<u> </u>	D6		
VS1			+5 +20	D	500 2H 550 25	150 1R 155 1E				
VT1 VTD	330000	33T	l	\vdash	600 26	160 1S				
VTG	1000000	10M	+10 +50	н	630 2J	165 1F 170 1T				
VZ2 VTL	100000	100	'	لــــا		180 1U 190 1V				
	1500000	15M				200 2L 215 2A				
	2200000	2284				210 2M				
	2200000	22M				220 2N 240 2Q				
	3300000	33M				250 2R 260 2S				
	'					270 2T				

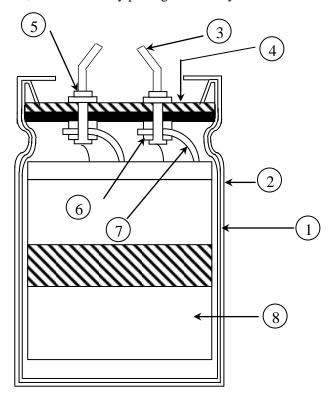
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3. Construction

Single ended type to be produced to fix the terminals to anode and cathode foil, and wind together with paper, and then wound element to be impregnated with electrolyte will be enclosed in an aluminum case. Finally sealed up tightly with end seal rubber, then finished by putting on the vinyl sleeve.



No	Component	Material
1	Case	Aluminum case
2	Sleeve	PET
3	Terminal	Solder coated copper clad steel
4	Seal	Rubber-laminated Bakelite
5	Rivet	Aluminum
6	Washer	Aluminum
7	Tab	Aluminum
8	Element	Aluminum foil & Electrolyte paper

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4. Characteristics

Standard atmospheric conditions

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests is as follows:

Ambient temperature :15°C to 35°C
Relative humidity : 45% to 85%
Air Pressure : 86kPa to 106kPa

If there is any doubt about the results, measurement shall be made within the following conditions:

Ambient temperature $: 20^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Relative humidity : 60% to 70%Air Pressure : 86kPa to 106kPa

Operating temperature range

The ambient temperature range at which the capacitor can be operated continuously at rated voltage See table 1 temperature range.

As to the detailed information, please refer to table 2

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	ITEM				PER	FORM	IANCE	Ξ				
		WV (V .DC)	10	16	25	35	50	63	80)	100	160
	Rated voltage (WV)	SV (V .DC)	13	20	32	44	63	79	10	0	125	200
4.1		WV (V.DC)	180	200	220	250	315	350	400	420	450	500
	Surge voltage (SV)	SV (V.DC)	225	250	270	300	365	400	450	470	500	550
4.2	Nominal capacitance (Tolerance)	Measuring Vo Measuring Te	<condition> Measuring Frequency : 120Hz±12Hz Measuring Voltage : Not more than 0.5Vrms Measuring Temperature : 20±2℃ <criteria> Shall be within the specified capacitance tolerance</criteria></condition>									
4.3	Leakage current	<condition> Connecting th minutes, and the <criteria> R</criteria></condition>	e capa	citor w	ith a p	rotectiv	ve resis) in ser	ies for
4.4	tanδ	<condition> See 4.2, Norm <criteria> R</criteria></condition>				asuring	g frequ	ency, v	oltage	and t	empera	ture.
4.5	Terminal strength	<condition> A static load of 25N (2.5kgf) shall be applied to the lead wire terminal in the axial direction away from the capacitor body for 30s <criteria> There shall be no intermittent contacts, open or short circuit and there shall be no mechanical damage such as terminal damage.</criteria></condition>										
		meemamea		_	as terr	nınal d	amage.	•				
		<condition< td=""><td>></td><td></td><td>as terr</td><td>nınal d</td><td>amage</td><td><u> </u></td><td></td><td></td><td></td><td></td></condition<>	>		as terr	nınal d	amage	<u> </u>				
				ng Tem			Time					
		< Condition		ng Tem	peratu		Time	e	ch the	rmal e	equilibr	
		<condition< td=""><td></td><td>20:</td><td>peratu</td><td></td><td>Time</td><td>e e to rea</td><td></td><td></td><td>equilibr</td><td>ium</td></condition<>		20:	peratu		Time	e e to rea			equilibr	ium
		<condition 1<="" step="" td=""><td></td><td>-40(-:</td><td>nperatu</td><td></td><td>Time Time</td><td>e to rea</td><td>ch the</td><td>rmal e</td><td></td><td>ium ium</td></condition>		-40(-:	nperatu		Time Time	e to rea	ch the	rmal e		ium ium
		<condition 1="" 2<="" step="" td=""><td></td><td>20: -40(-2</td><td>1 peratu: ±2 25)±3</td><td></td><td>Time Time</td><td>e to rea</td><td>ch the</td><td>rmal e rmal e</td><td>equilibr</td><td>ium ium ium</td></condition>		20: -40(-2	1 peratu: ±2 25)±3		Time Time	e to rea	ch the	rmal e rmal e	equilibr	ium ium ium
		Condition STEP 1 2 3		20: -40(-2 20 105	nperatu: ±2 25)±3 ±2		Time Time Time Time	e to rea	ch then	rmal e rmal e rmal e	equilibr equilibr	ium ium ium ium
		<condition< td=""><td>Testi</td><td>20: -40(-20) 105 20</td><td>12 ± 2 ± 2 ± 2 ± 2 ± 2 ± 2 ± 2</td><td>re(°C)</td><td>Time Time Time Time Time</td><td>e to rea e to rea e to rea e to rea e to rea</td><td>ch then</td><td>rmal e rmal e rmal e</td><td>equilibr equilibr equilibr</td><td>ium ium ium ium</td></condition<>	Testi	20: -40(-20) 105 20	12 ± 2 ± 2 ± 2 ± 2 ± 2 ± 2 ± 2	re(°C)	Time Time Time Time Time	e to rea e to rea e to rea e to rea e to rea	ch then	rmal e rmal e rmal e	equilibr equilibr equilibr	ium ium ium ium
4.6	Temperature	<condition 1="" 2="" 3="" 4="" 5="" <criteria="" step=""></condition>	Testi	20: -40(-20) 105 20 nall be s	$\begin{array}{c} \text{aperature} \\ \pm 2 \\ 25) \pm 3 \\ \pm 2 \\ \pm 2 \\ \pm 2 \\ \end{array}$ $\begin{array}{c} \pm 2 \\ \pm 2 \\ \end{array}$ within	re(°C)	Time Time Time Time Time Time Time	e to rea e to rea e to rea e to rea e to rea e to rea e to rea	ch then ch then ch then	rmal e rmal e rmal e	equilibr equilibr equilibr equilibr	ium ium ium ium ium
4.6	Temperature characteristics	<condition< td=""><td>Testi</td><td>20: -40(-2 20 105 20 nall be st t measu</td><td>$\begin{array}{c} \text{aperatur} \\ \pm 2 \\ 25) \pm 3 \\ \pm 2 \\ \pm 2 \\ \end{array}$ $\begin{array}{c} \pm 2 \\ \pm 2 \\ \end{array}$ within ared shall</td><td>$re(^{\circ}C)$ the limall not</td><td>Time Time Time Time Time Time Time more the</td><td>e to rea e to rea e to rea e to rea e to rea e to rea e m 4.4 han 8 t</td><td>ch then ch then ch then ch then</td><td>rmal e rmal e rmal e</td><td>equilibr equilibr equilibr equilibr</td><td>ium ium ium ium ium</td></condition<>	Testi	20: -40(-2 20 105 20 nall be st t measu	$\begin{array}{c} \text{aperatur} \\ \pm 2 \\ 25) \pm 3 \\ \pm 2 \\ \pm 2 \\ \end{array}$ $\begin{array}{c} \pm 2 \\ \pm 2 \\ \end{array}$ within ared shall	$re(^{\circ}C)$ the limall not	Time Time Time Time Time Time Time more the	e to rea e to rea e to rea e to rea e to rea e to rea e m 4.4 han 8 t	ch then ch then ch then ch then	rmal e rmal e rmal e	equilibr equilibr equilibr equilibr	ium ium ium ium ium
4.6		<condition 1="" 2="" 3="" 4="" 5="" <criteria="" step=""> The leakage</condition>	Testi	20: -40(-20) 105 20) nall be st t measu shall t	nperature ± 2 $25)\pm 3$ ± 2 ± 2 ± 2 within pred show the with	re(°C) the lim all not in the l	Time t	e to rea e to rea e to rea e to rea e to rea em 4.4 han 8 t	ch then ch then ch then ch then imes o	rmal ormal ormal ormal ormal of	equilibr equilibr equilibr equilibr	ium ium ium ium ium
4.6		Condition STEP 1 2 3 4 5 Criteria> The leakage a. In step The lea b. At-40°C	Testi tanδ sl curren 5, tanδ kage ci (-25°C)	20: -40(-20) 105 20 nall be t measured the shall the turrent solo, impedia	aperature ± 2 $25)\pm 3$ ± 2 ± 2 ± 2 within ared shown with a chall no	the limall not in the lot more	Time than than the	e to rea e to fea e t	ch then ch then ch then ch then imes o	rmal ormal ormal ormal of its s	equilibr equilibr equilibr equilibr pecified	ium ium ium ium ium
4.6		<condition 1="" 2="" 3="" 4="" 5="" <criteria="" step=""> The leakage a. In step The lea b. At-40°C followin</condition>	Testi tanδ sl curren 5, tanδ kage c (-25°C) g table	20: -40(-; 20: 105: 20: nall be at measure shall the turrent	nperature ± 2 $25)\pm 3$ ± 2 ± 2 within arred shape with shall no dance (the limall not in the lot more Z) ratio	Time Time Time Time Time Time Time tit of Ite more the imit of than the o shall	e to rea e to rea e to rea e to rea e to rea e to rea em 4.4 han 8 t Titem 4 ne spec not exc	ch then ch then ch then ch then ch then imes o 1.4 ified v ceed th	rmal ormal o	equilibr equilibr equilibr equilibr pecifiec ue of the	ium ium ium ium ium
4.6		Condition STEP 1 2 3 4 5 Criteria> The leakage a. In step The lea b. At-40°C followin Working	Testi tanδ sl curren 5, tanδ kage ci (-25°C) g table Voltag	20: -40(-; 20 105 20 nall be t measured the shall the trurrent solo, impedience to the shall the trurrent solo, impedience to the shall the s	aperature ± 2 $25)\pm 3$ ± 2 ± 2 ± 2 within ared shown with a chall no	the limall not in the lot more Z) ratio	Time Time Time Time Time Time Time time time to shall	e to rea o to rea e t	ch then ch the ch then	rmal ermal e	equilibrequili	ium ium ium ium ium ium ium ium
4.6		Condition STEP 1 2 3 4 5 Criteria> The leakage a. In step The lea b. At-40°C followin Working Z-25°C	Testi tanδ sl curren 5, tanδ kage c (-25°C) g table Voltaş C/Z+20	20: -40(-: 20 105 20 nall be t measus shall to the s	nperature ± 2 $25)\pm 3$ ± 2 ± 2 within arred shape with shall no dance (the limall not in the lot more Z) ratio	Time Time Time Time Time Time Time tit of Ite more the imit of than the o shall	e e to rea e	ch then ch the ch then	rmal ormal o	equilibrequili	ium ium ium ium ium
4.6		Condition STEP 1 2 3 4 5 Criteria> The leakage a. In step The lea b. At-40°C followin Working Z-25°C	Testi tanδ sl curren 5, tanδ kage ci (-25°C) g table Voltag	20: -40(-: 20 105 20 nall be t measus shall to the contract of the contra	nperature ± 2 $25)\pm 3$ ± 2 ± 2 within need show the shall no dance (the limal not in the lot more Z) ratio	Time Time Time Time Time Time Time time time to shall	e to rea o to rea e t	ch then ch the ch then	rmal ermal e	equilibrequili	ium ium ium ium ium ium ium ium

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4.7	Load life test	temperature of 105 °C for 2000 +48/0 hours. (The sum of working voltage) Then the productime at atmospheric consciences	4-4No.4.13 methods, The capacitor is stored at a ±2 with DC bias voltage plus the rated ripple current of DC and ripple peak voltage shall not exceed the rated uct should be tested after16 hours recovering anditions. The result should meet the following table: I meet the following requirements. Value in 4.3 shall be satisfied Within ±20% of initial value. Not more than 200% of the specified value. There shall be no leakage of electrolyte
4.8	Shelf life test	±2°C for 1000+48/0 ho Following this period the be allowed to stabilized Next they shall be conne rated voltage applied for and then, tested the char < Criteria> The characteristic shall Leakage current Capacitance Change tanδ Appearance Remark: If the capac may increase. Please	e capacitors shall be removed from the test chamber and at room temperature for $4{\sim}8$ hours. ected to a series limiting resistor($1k\pm100\Omega$) with D.C. 30min. After which the capacitors shall be discharged,
4.9	Surge test	resistor. The capacitor shall be s 30 ±5s, followed discha The test temperature sha C _R :Nominal Capacitanc < Criteria> Leakage current Capacitance Change tanδ Appearance Attention: This test simulates over	all be 15~35°C.
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4.10	Vibration test	perpendicular direction Vibration frequency is Peak to peak amplitude Sweep rate <criteria> After the test, the fole Appearance Inner construction</criteria>	ons. range: 10Hz de: 1.5mi : 10Hz lowing item No mechan electrolyte of the legible. No intermit No damage	m z ~ 55Hz ~ 10Hz in about 1 minute
4.11	Solderabilit y test	Soldering temperature Dipping depth Dipping speed Dipping time <criteria></criteria>	tested under	the following conditions: : 245±3°C : 2mm : 25±2.5mm/s : 3±0.5s A minimum of 95% of the surface being
4.12	Resistance to solder heat	260±5°C for 10±1 sec body of capacitor. Then the capacitor sha humidity for 1~2 hour Criteria >	conds or400 all be left un rs before me	
	test	Leakage current Capacitance Change tanδ Appearance	Wit Not	more than the specified value. hin $\pm 10\%$ of initial value. more than the specified value. ere shall be no leakage of electrolyte

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		<condition> Temperature Cycle: According to IEC60384-4No.4.7 methods, capacitor shall be placed in an oven, the condition according as below:</condition>
	Temperature Time	
		(1)+20°C ≤ 3 Minutes
	Change of	(2)Rated low temperature(-40°C) (-25°C) 30 ± 2 Minutes
4.13	temperature	(3)Rated high temperature (+105°C) 30 ± 2 Minutes
	test	(1) to (3)=1 cycle, total 5 cycle
		<criteria></criteria>
		The characteristic shall meet the following requirement
		Leakage current Not more than the specified value.
		$tan\delta$ Not more than the specified value.
		Appearance There shall be no leakage of electrolyte
4.14	Damp heat test	$40\pm2^{\circ}$ C, the characteristic change shall meet the following requirement. Criteria> Leakage current Not more than the specified value. Capacitance Change Within $\pm20\%$ of initial value . $\tan\delta$ Not more than 120% of the specified value. Appearance There shall be no leakage of electrolyte.
4.15	Vent test	Condition> The following test only apply to those products with vent. D.C. test The capacitor is connected with its polarity reversed to a DC power source. Then a current selected from Table 2 is applied. Table 3> Diameter (mm) DC Current (A) 22.4 or less 1 Over 22.4 10 Criteria> The vent shall operate with no dangerous conditions such as flames of dispersion of pieces of the capacitor and/or case.

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<Condition> The maximum permissible ripple current is the maximum A.C current at 120Hz and can be applied at maximum operating temperature The combined value of D.C voltage and the peak A.C voltage shall not exceed the rated voltage and shall not reverse voltage. Frequency Multipliers: Freq. Coefficient (Hz) 60 120 1k 10~50k Maximum permissible Voltage (V) 4.16 (ripple 10~100V 0.90 1.00 1.15 1.25 current) 160~250V 0.80 1.00 1.25 1.47 315~500V 0.80 1.00 1.30 1.47

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5. It refers to the latest document of "Environment-related Substances standard" (WI-HSPM-QA-072).

	Substances
	Cadmium and cadmium compounds
Heavy metals	Lead and lead compounds
Heavy metais	Mercury and mercury compounds
	Hexavalent chromium compounds
	Polychlorinated biphenyls (PCB)
Chloinated	Polychlorinated naphthalenes (PCN)
organic	Polychlorinated terphenyls (PCT)
compounds	Short-chain chlorinated paraffins(SCCP)
	Other chlorinated organic compounds
D : 1	Polybrominated biphenyls (PBB)
Brominated	Polybrominated diphenylethers(PBDE) (including
organic	decabromodiphenyl ether[DecaBDE])
compounds	Other brominated organic compounds
Tributyltin compo	ounds(TBT)
Triphenyltin com	pounds(TPT)
Asbestos	
Specific azo comp	pounds
Formaldehyde	
Polyvinyl chlorid	e (PVC) and PVC blevds
Beryllium oxide	
Beryllium coppe	er
Specific phthalate	es (DEHP,DBP,BBP,DINP,DIDP,DNOP,DNHP)
Hydrofluorocarbo	on (HFC), Perfluorocarbon (PFC)
Perfluorooctane s	ulfonates (PFOS)
Specific Benzotri	azole

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Attachment: Application Guidelines

1. Circuit Design

1.1 Operating Temperature and Frequency

Electrolytic capacitor electrical parameters are normally specified at 20° C temperature and 120Hz frequency. These parameters vary with changes in temperature and frequency. Circuit designers should take these changes into consideration.

- (1) Effects of operating temperature on electrical parameters
 - a) At higher temperatures, leakage current and capacitance increase while equivalent series resistance (ESR) decreases.
 - b) At lower temperatures, leakage current and capacitance decrease while equivalent series resistance (ESR) increases.
- (2) Effects of frequency on electrical parameters
 - a) At higher frequencies capacitance and impedance decrease while $tan\delta$ increases.
 - b) At lower frequencies, ripple current generated heat will rise due to an increase in equivalent series resistance (ESR).
- 1.2 Operating Temperature and Life Expectancy

See the file: Life calculation of aluminum electrolytic capacitor

1.3 Common Application Conditions to Avoid

The following misapplication load conditions will cause rapid deterioration to capacitor electrical parameters. In addition, rapid heating and gas generation within the capacitor can occur causing the pressure relief vent to operate and resultant leakage of electrolyte. Under Leaking electrolyte is combustible and electrically conductive.

(1) Reverse Voltage

DC capacitors have polarity. Verify correct polarity before insertion. For circuits with changing or uncertain polarity, use DC bipolar capacitors. DC bipolar capacitors are not suitable for use in AC circuits.

(2) Charge / Discharge Applications

Standard capacitors are not suitable for use in repeating charge / discharge applications. For charge / discharge applications consult us and advise actual conditions.

(3) Over voltage

Do not apply voltages exceeding the maximum specified rated voltage. Voltages up to the surge voltage rating are acceptable for short periods of time. Ensure that the sum of the DC voltage and the superimposed AC ripple voltage does not exceed the rated voltage.

(4) Ripple Current

Do not apply ripple currents exceeding the maximum specified value. For high ripple current applications, use a capacitor designed for high ripple currents or contact us with your requirements.

Ensure that allowable ripple currents superimposed on low DC bias voltages do not cause reverse voltage conditions.

1.4 Using Two or More Capacitors in Series or Parallel

(1) Capacitors Connected in Parallel

The circuit resistance can closely approximate the series resistance of the capacitor causing an imbalance of ripple current loads within the capacitors. Careful design of wiring methods can minimize the possibility of excessive ripple currents applied to a capacitor.

(2) Capacitors Connected in Series

Normal DC leakage current differences among capacitors can cause voltage imbalances. The use of voltage divider shunt resistors with consideration to leakage current can prevent capacitor voltage imbalances.

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1.5 Capacitor Mounting Considerations

(1) Double Sided Circuit Boards

Avoid wiring pattern runs, which pass between the mounted capacitor and the circuit board.

When dipping into a solder bath, excess solder may collect under the capacitor by capillary action and short circuit the anode and cathode terminals.

(2) Circuit Board Hole Positioning

The vinyl sleeve of the capacitor can be damaged if solder passes through a lead hole for subsequently processed parts. Special care when locating hole positions in proximity to capacitors is recommended.

(3) Circuit Board Hole Spacing

The circuit board holes spacing should match the capacitor lead wire spacing within the specified tolerances. Incorrect spacing can cause excessive lead wire stress during the insertion process. This may result in premature capacitor failure due to short or open circuit, increased leakage current, or electrolyte leakage.

(4) Clearance for Case Mounted Pressure Relief vents

Capacitors with case mounted pressure relief vents require sufficient clearance to allow for proper vent operation. The minimum clearances are dependent on capacitor diameters as proper vent operation. The minimum clearances are dependent on capacitor diameters as follows.

φ 6.3~φ 16mm:2mm minimum, φ 18~φ 35mm:3mm minimum, φ 40mm or greater:5mm minimum.

(5) Clearance for Seal Mounted Pressure Relief Vents

A hole in the circuit board directly under the seal vent location is required to allow proper release of pressure.

(6) Wiring Near the Pressure Relief Vent

Avoid locating high voltage or high current wiring or circuit board paths above the pressure relief vent. Flammable, high temperature gas exceeding 100°C may be released which could dissolve the wire insulation and ignite.

(7) Circuit Board patterns Under the Capacitor

Avoid circuit board runs under the capacitor as electrolyte leakage could cause an electrical short.

(8) Screw Terminal Capacitor Mounting

Do not orient the capacitor with the screw terminal side of the capacitor facing downwards.

Tighten the terminal and mounting bracket screws within the torque range specified in the specification.

1.6 Electrical Isolation of the Capacitor

Completely isolate the capacitor as follows.

- (1) Between the cathode and the case (except for axially leaded B types) and between the anode terminal and other circuit paths
- (3) Between the extra mounting terminals (on T types) and the anode terminal, cathode terminal, and other circuit paths.
- 1.7 The Product characteristic should take the sample as the standard.

1.8 Capacitor Sleeve

The vinyl sleeve or laminate coating is intended for marking and identification purposes and is not meant to electrically insulate the capacitor.

The sleeve may split or crack if immersed into solvents such as toluene or xylene, and then exposed to high temperatures.

CAUTION!

Always consider safety when designing equipment and circuits. Plan for worst case failure modes such as short circuits and open circuits which could occur during use.

- (1) Provide protection circuits and protection devices to allow safe failure modes.
- (2) Design redundant or secondary circuits where possible to assure continued operation in case of main circuit failure.

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2. Capacitor Handling Techniques

- 2.1 Considerations Before Using
- (1) Capacitors have a finite life. Do not reuse or recycle capacitors from used equipment.
- (2) Transient recovery voltage may be generated in the capacitor due to dielectric absorption. If required, this voltage can be discharged with a resistor with a value of about $1k\Omega$.
- (3) Capacitors stored for long periods of time may exhibit an increase in leakage current. This can be corrected by gradually applying rated voltage in series with a resistor of approximately $1k\Omega$.
- (4) If capacitors are dropped, they can be damaged mechanically or electrically. Avoid using dropped capacitors.
- (5) Dented or crushed capacitors should not be used. The seal integrity can be compromised and loss of electrolyte / shortened life can result.

2.2 Capacitor Insertion

- * (1) Verify the correct capacitance and rated voltage of the capacitor.
- * (2) Verify the correct polarity of the capacitor before inserting.
- * (3) Verify the correct hole spacing before insertion (land pattern size on chip type) to avoid stress on the terminals.
 - (4) Ensure that the auto insertion equipment lead clinching operation does not stress the capacitor leads where they enter the seal of the capacitor.

For chip type capacitors, excessive mounting pressure can cause high leakage current, short circuit, or disconnection.

2.3 Manual Soldering

- (1) Observe temperature and time soldering specifications or do not exceed temperatures of 400 °C for 3 seconds or less.
- (2) If lead wires must be formed to meet terminal board hole spacing, avoid stress on the lead wire where it enters the capacitor seal.
- (3) If a soldered capacitor must be removed and reinserted, avoid excessive stress to the capacitor leads.
- (4) Avoid touching the tip of the soldering iron to the capacitor, to prevent melting of the vinyl sleeve.

2.4 Flow Soldering

- (1) Do not immerse the capacitor body into the solder bath as excessive internal pressure could result.
- (2) Observe proper soldering conditions (temperature, time, etc.) Do not exceed the specified limits.
- (3) Do not allow other parts or components to touch the capacitor during soldering.

2.5 Other Soldering Considerations

Rapid temperature rises during the preheat operation and resin bonding operation can cause cracking of the capacitor vinyl sleeve.

For heat curing, do not exceed 150°C for a maximum time of 2 minutes.

2.6 Capacitor Handling after Solder

- (1). Avoid movement of the capacitor after soldering to prevent excessive stress on the lead wires where they enter the seal.
- (2). Do not use capacitor as a handle when moving the circuit board assembly.
- (3). Avoid striking the capacitor after assembly to prevent failure due to excessive shock.

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2.7 Circuit Board Cleaning

* (1) Circuit boards can be immersed or ultrasonically cleaned using suitable cleaning solvents for up 5 minutes and up to 60° C maximum temperatures. The boards should be thoroughly rinsed and dried.

The use of ozone depleting cleaning agents is not recommended in the interest of protecting the environment.

- * (2) Avoid using the following solvent groups unless specifically allowed for in the specification;
- Halogenated cleaning solvents: except for solvent resistant capacitor types, halogenated solvents can permeate the seal and cause internal capacitor corrosion and failure. For solvent resistant capacitors, carefully follow the temperature and time requirements of the specification. 1-1-1 trichloroethane should never be used on any aluminum electrolytic capacitor.
- Alkali solvents : could attack and dissolve the aluminum case.
- Petroleum based solvents: deterioration of the rubber seal could result.
- . Xylene : deterioration of the rubber seal could result.
- . Acetone : removal of the ink markings on the vinvl sleeve could result.
- * (3) A thorough drying after cleaning is required to remove residual cleaning solvents which may be trapped between the capacitor and the circuit board. Avoid drying temperatures, which exceed the maximum rated temperature of the capacitor.
- * (4) Monitor the contamination levels of the cleaning solvents during use by electrical conductivity, pH, specific gravity, or water content. Chlorine levels can rise with contamination and adversely affect the performance of the capacitor.

Please consult us for additional information about acceptable cleaning solvents or cleaning methods.

2.8 Mounting Adhesives and Coating Agents

When using mounting adhesives or coating agents to control humidity, avoid using materials containing halogenated solvents. Also, avoid the use of chloroprene based polymers.

After applying adhesives or coatings, dry thoroughly to prevent residual solvents from being trapped between the capacitor and the circuit board.

3. Precautions for using capacitors

3.1 Environmental Conditions

Capacitors should not be stored or used in the following environments.

- * (1) Temperature exposure above the maximum rated or below the minimum rated temperature of the capacitor.
- * (2) Direct contact with water, salt water, or oil.
- * (3) High humidity conditions where water could condense on the capacitor.
- * (4) Exposure to toxic gases such as hydrogen sulfide, sulfuric acid, nitric acid chlorine, or ammonia.
- * (5) Exposure to ozone, radiation, or ultraviolet rays.
- * (6) Vibration and shock conditions exceeding specified requirements.

3.2 Electrical Precautions

- (1) Avoid touching the terminals of the capacitor as possible electric shock could result. The exposed aluminum case is not insulated and could also cause electric shock if touched.
- (2) Avoid short circuit the area between the capacitor terminals with conductive materials including liquids such as acids or alkaline solutions.

4. Emergency Procedures

- (1) If the pressure relief vent of the capacitor operates, immediately turn off the equipment and disconnect form the power source. This will minimize additional damage caused by the vaporizing electrolyte.
- (2) Avoid contact with the escaping electrolyte gas which can exceed 100°C temperatures.

If electrolyte or gas enters the eye, immediately flush the eyes with large amounts of water.

If electrolyte or gas is ingested by month, gargle with water.

If electrolyte contacts the skin, wash with soap and water.

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5. Long Term Storage

Leakage current of a capacitor increases with long storage times. The aluminum oxide film deteriorates as a function of temperature and time. If used without reconditioning, an abnormally high current will be required to restore the oxide film. This current surge could cause the circuit or the capacitor to fail.

After one year, a capacitor should be reconditioned by applying rated voltage in series with a 1000Ω , current limiting resistor for a time period of 30 minutes .

5.1 Environmental Conditions

The capacitor shall be not use in the following condition:

- (1) Temperature exposure above the maximum rated or below the minimum rated temperature of the capacitor.
- (2) Direct contact with water, salt water, or oil.
- (3) High humidity conditions where water could condense on the capacitor.
- (4) Exposure to toxic gases such as hydrogen sulfide, sulfuric acid, nitric acid, chlorine, or ammonia.
- (5) Exposure to ozone, radiation, or ultraviolet rays.
- (6) Vibration and shock conditions exceeding specified requirements.

6. Capacitor Disposal

When disposing of capacitors, use one of the following methods.

- * Incinerate after crushing the capacitor or puncturing the can wall (to prevent explosion due to internal pressure rise). Capacitors should be incinerated at high temperatures to prevent the release of toxic gases such as chlorine from the polyvinyl chloride sleeve, etc.
- * Dispose of as solid waste.

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